

**Amendments To The Specification**

Please replace page 10, lines 22-31 in the Specification with the following amended paragraph:

**~~FIG. 3~~ FIG. 8** is a circuit diagram of a third embodiment of the present invention.

In this embodiment, primary terminals of a piezoelectric step-up transformer 10 and a ferrite step-down transformer 12 are connected in series to an output terminal of a half-bridge MOSFET switch 17, so that DC-AC conversion is eliminated from a secondary power consumption block 7, thereby increasing the power supply efficiency. Similar to the second embodiment, since power consumption of each of a display control circuit 14, a VCO 19, and a comparator 23 is low, buck regulators 24 and 25 are used and stabilize a DC voltage that is input to these circuits.